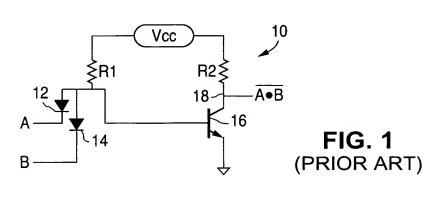


Title: Integrated Schottky Transistor Logic Configuration Inventors: Thomas S. Wong et al. Application No.: 10/665,645





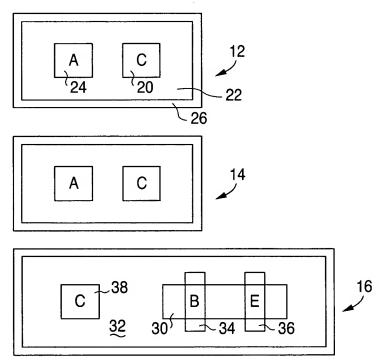
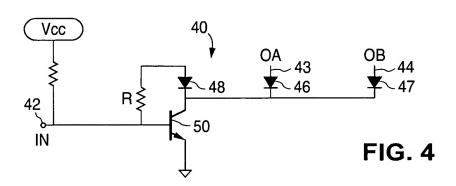
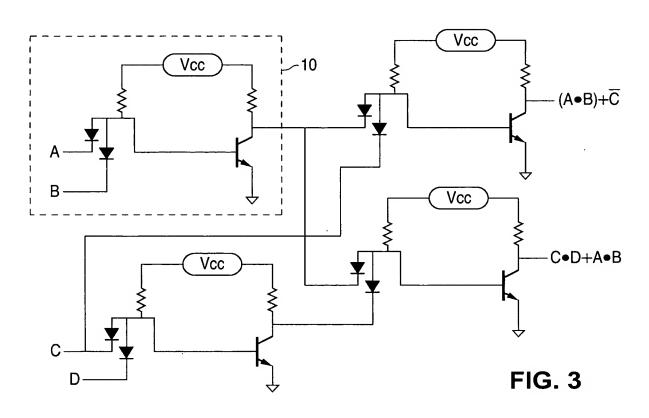


FIG. 2 (PRIOR ART)







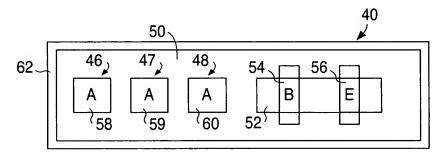


FIG. 5

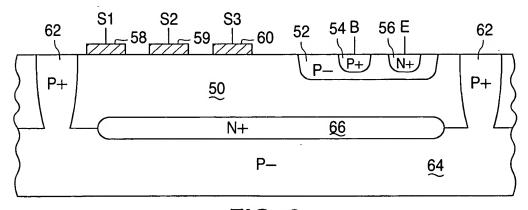
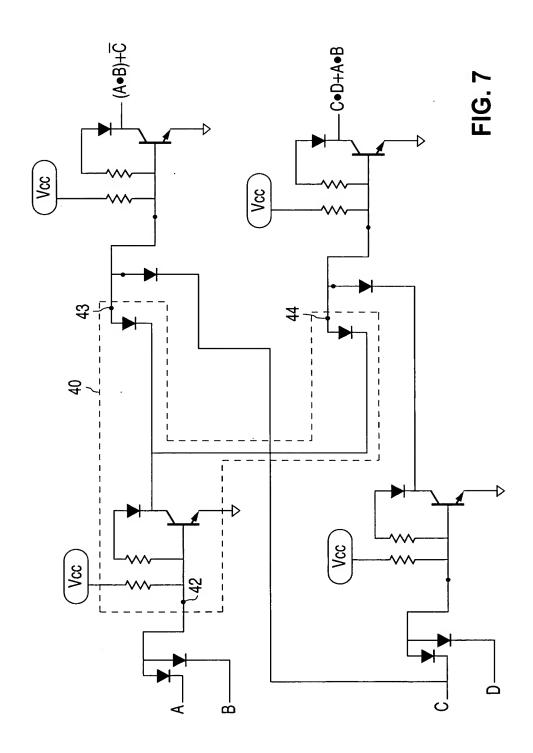


FIG. 6







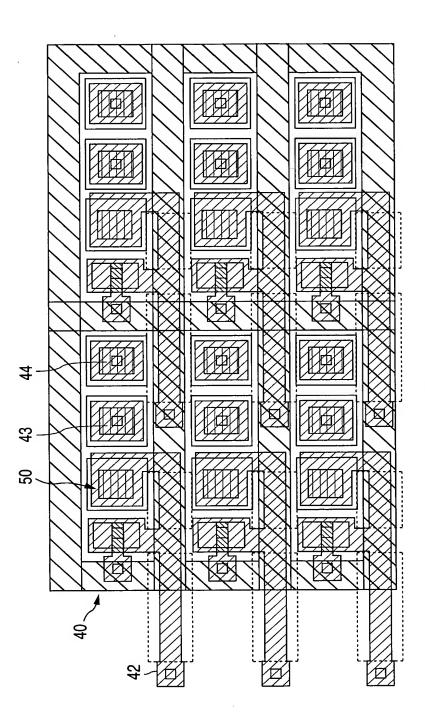


FIG. 8



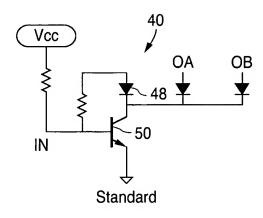
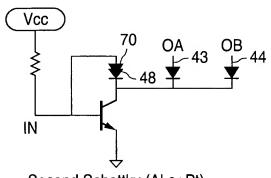


FIG. 9



Second Schottky (Al or Pt)

FIG. 10

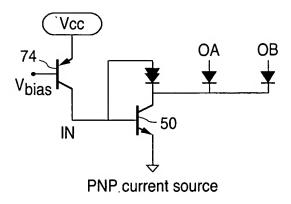


FIG. 11

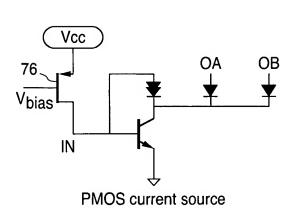


FIG. 12